

Fig. 1
PRIOR ART

Header A
Data A
Header B
Data 1B
Data 2B
Data 3B
Data 4B
Header C
Data 1C
:

Bitstream header Config. Logic Reg. address	Config. Logic Register data contents
0000	Cyclic Redundancy Check (CRC)
0001	Frame Address
0010	Frame Data Input
0011	Frame Data Output
0100	Command
0110	Control
0111	Status
1000	Daisy Chain Output
1001	Configuration Option
1010	Reserved
1011	Frame Length
1100	Reserved
1101	Reserved
1110	Reserved
1111	Reserved

Fig. 2a
PRIOR ART

Fig. 2d
PRIOR ART

32-bit Command Header Format 1

Format Type	Op code	Config. Logic Register address	Word count
0 0 1	0 0 0 1 1 0		

Fig. 2b PRIOR ART

11 bits, counts 2048 words

32-bit Command Header Format 1

Format Type	Op code	Config. Logic Register Address	Word Count
0 0 1	0 0 0 1 1 0		0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

32-bit Command Header Format 2

Format Type	Op code	Word Count
0 1 0	0 0 0 1 1 0	

27 bits, counts 2.68 million words or 10 megabytes

Fig. 2c PRIOR ART

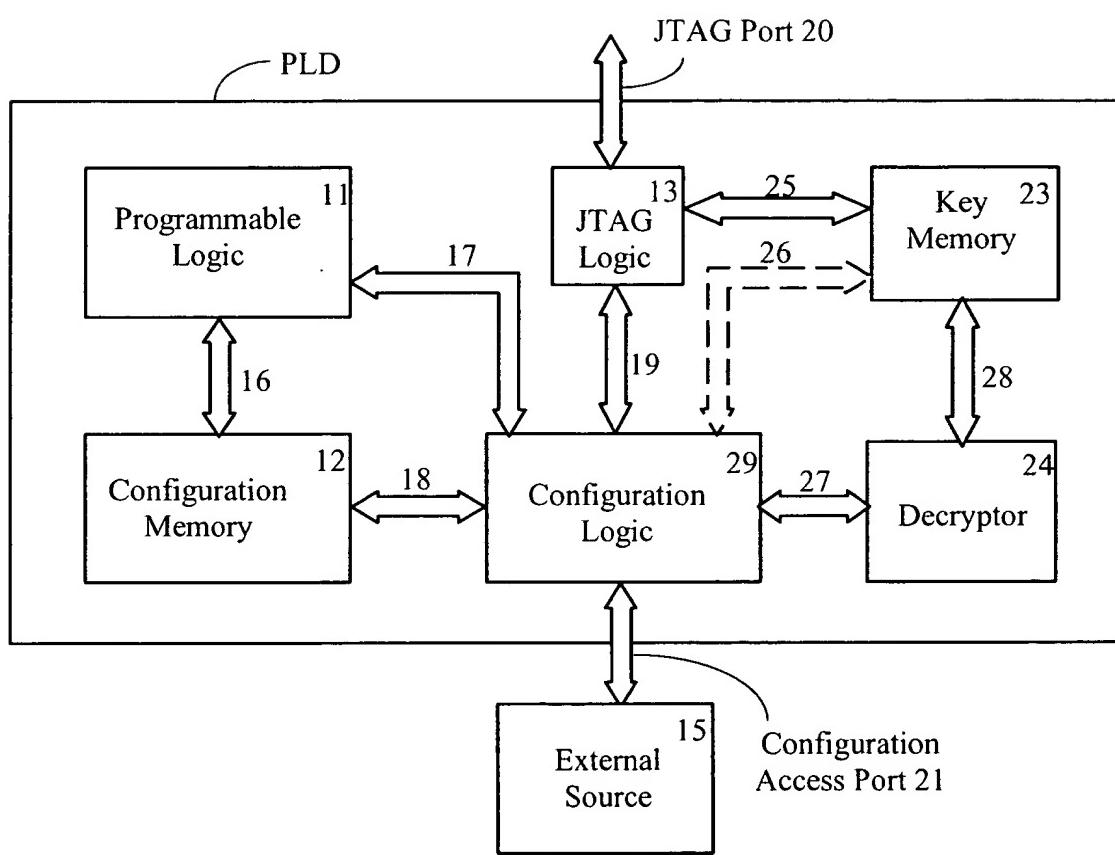


Fig. 3

Header A
Data A
Header B
Data B
Header C
Data C
Header D
Data 1D
Data 2D
Data 3D
⋮

Fig. 4a

Bitstream header Config. Logic Reg. address	Config. Logic Register data contents
0000	Cyclic Redundancy Check (CRC)
0001	Frame Address
0010	Frame Data Input
0011	Frame Data Output
0100	Command
0110	Control
0111	Status
1000	Daisy Chain Output
1001	Configuration Option
1010	Reserved
1011	Frame Length
1100	Cipher block chaining(CBC) starter
1101	Initial key address
1110	Reserved
1111	Reserved

Fig. 4d

32-bit Command Header Format 1

Fig. 4b

11 bits, counts 2048 words

32-bit Command Header Format 1

Format Type	Op code	Config. Logic Register Address																Word Count							
0 0 1	0 0 0 1 1 0 1 1																	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0							

32-bit Command Header Format 2

Format Type	Op code	Word Count
0	00	
1	01	
0	10	
1	11	

Fig. 4c

27 bits, counts 2.68 million words or 10 megabytes

UNENCRYPTED VERSION

Fig. 5a

ENCRYPTED VERSION

Fig. 5b

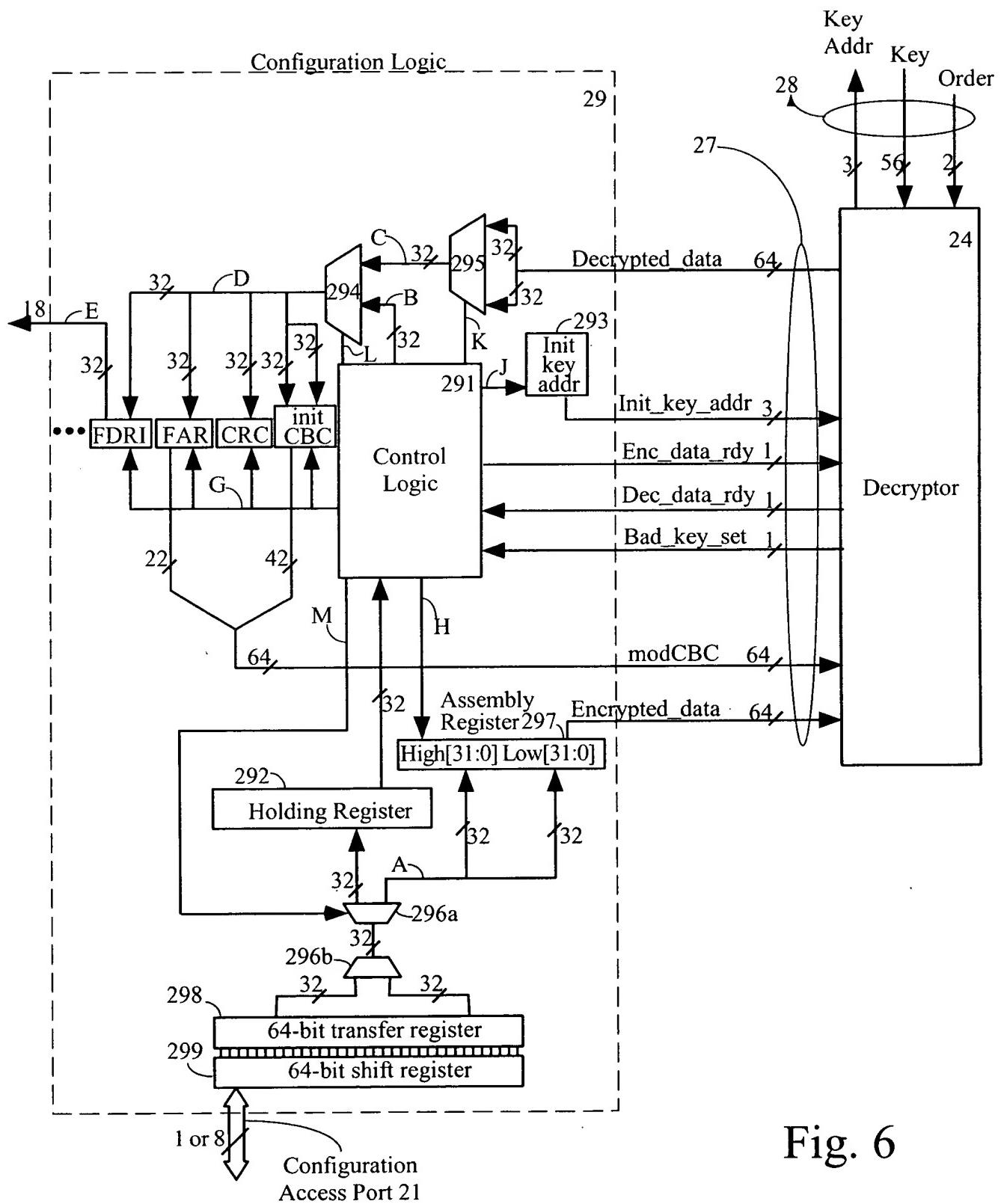


Fig. 6

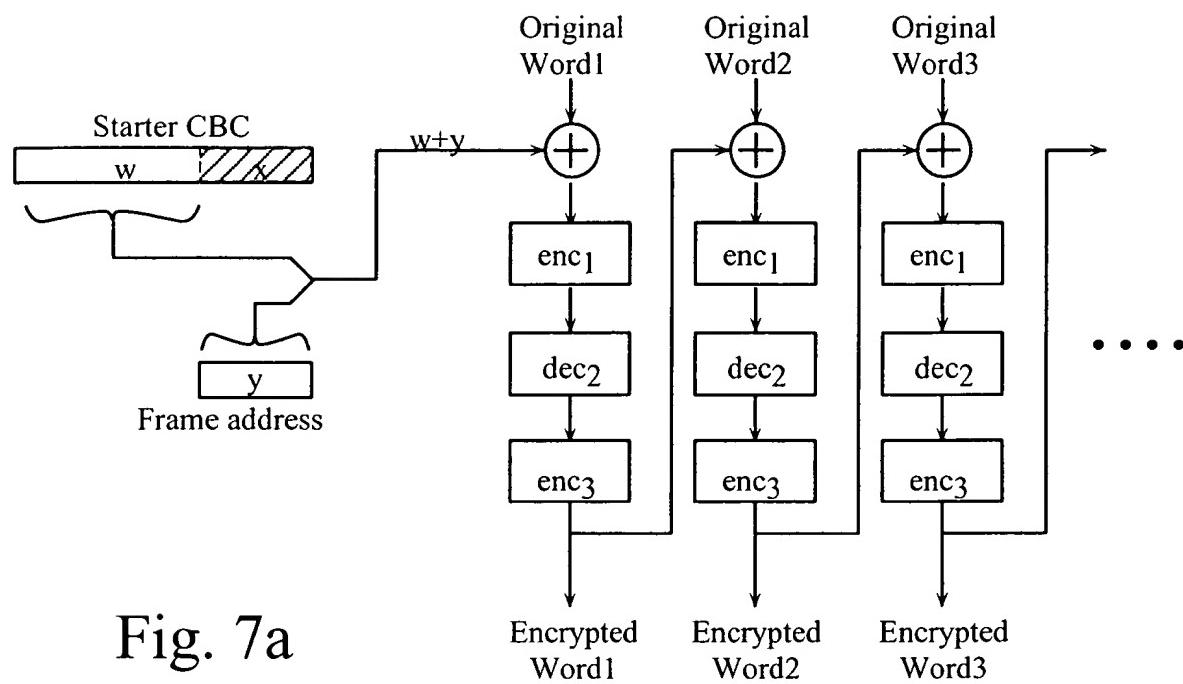


Fig. 7a

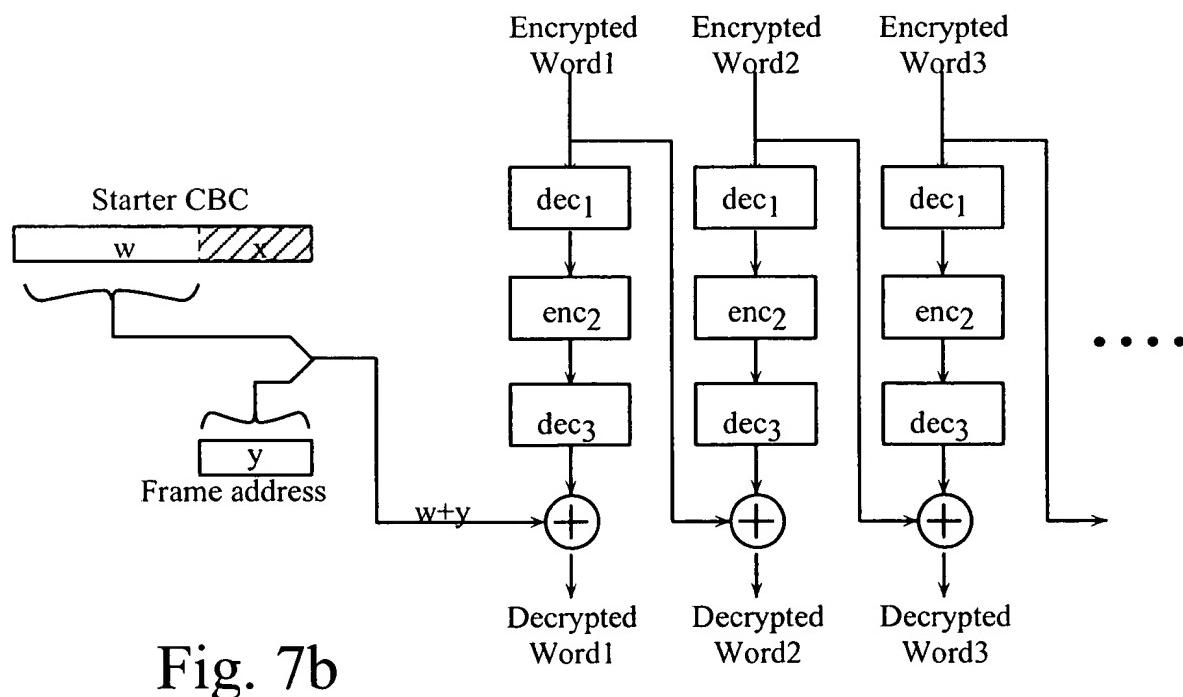


Fig. 7b

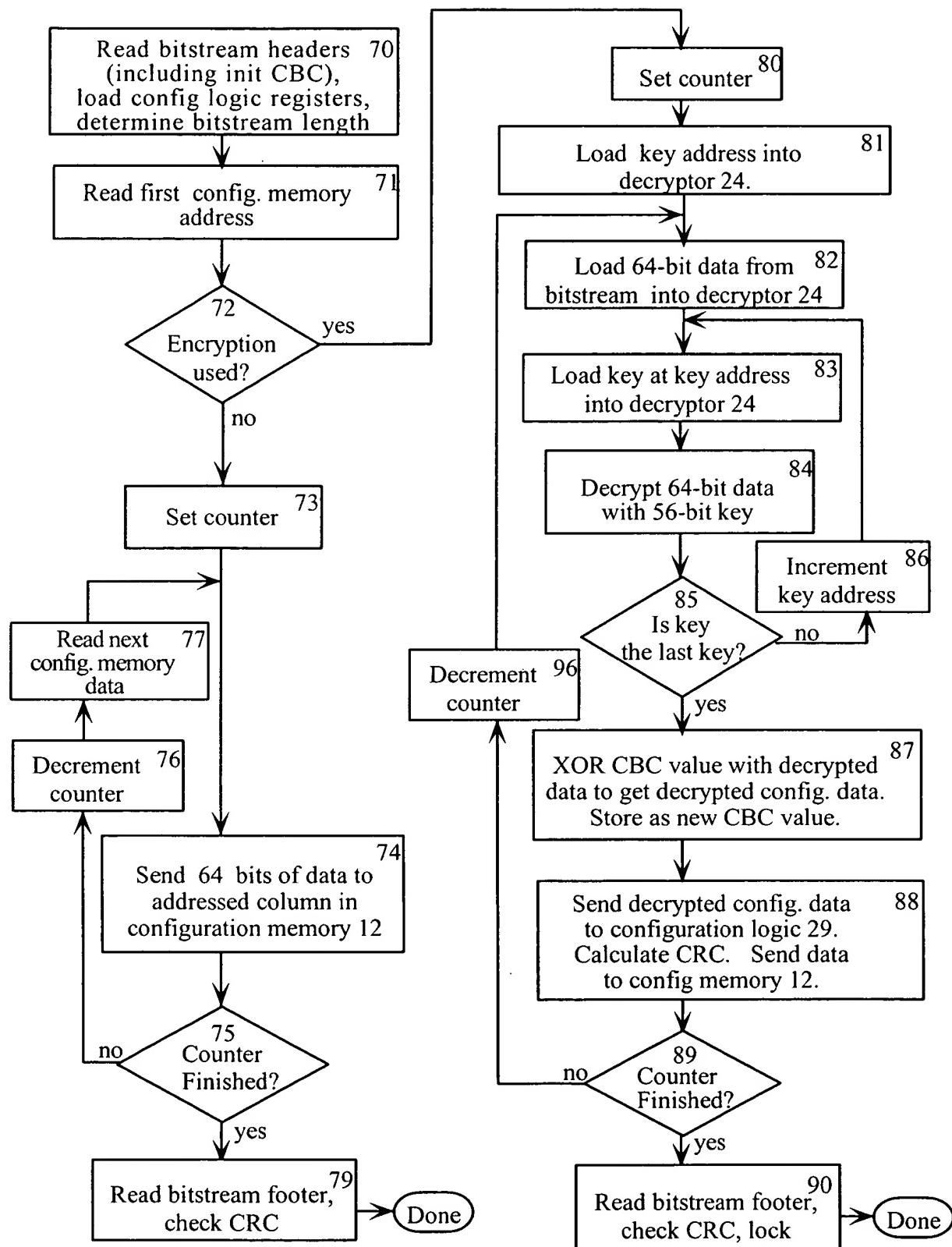


Fig. 8

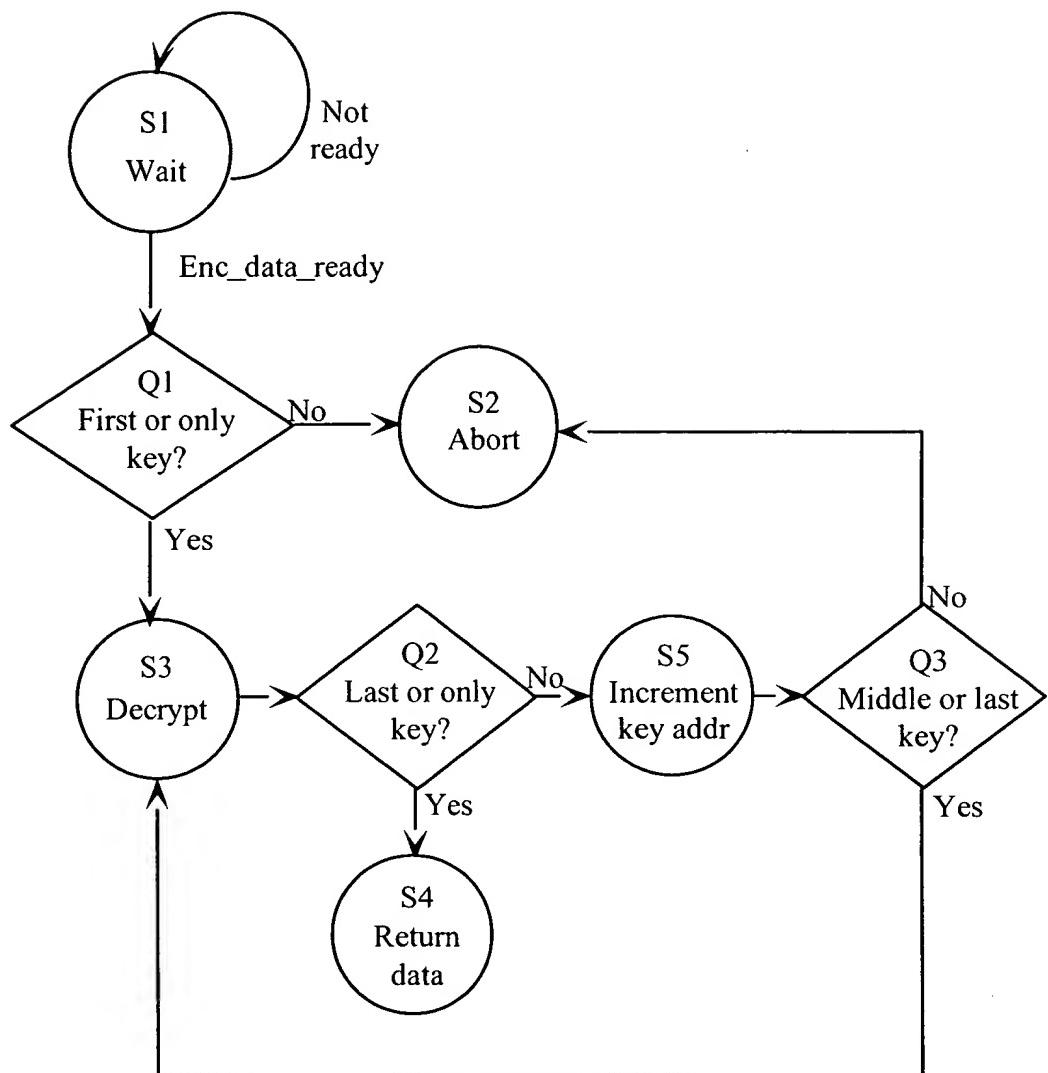
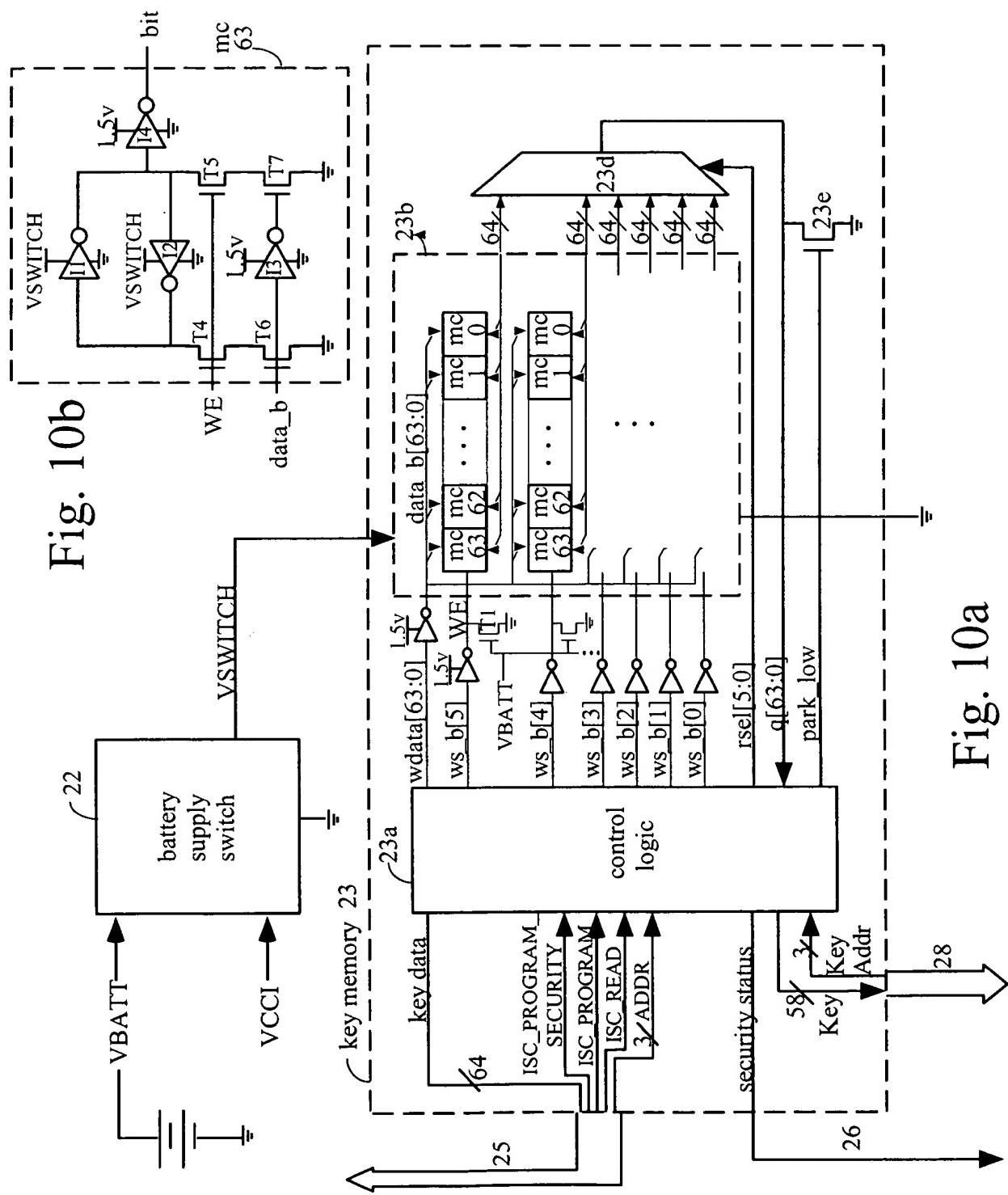


Fig. 9



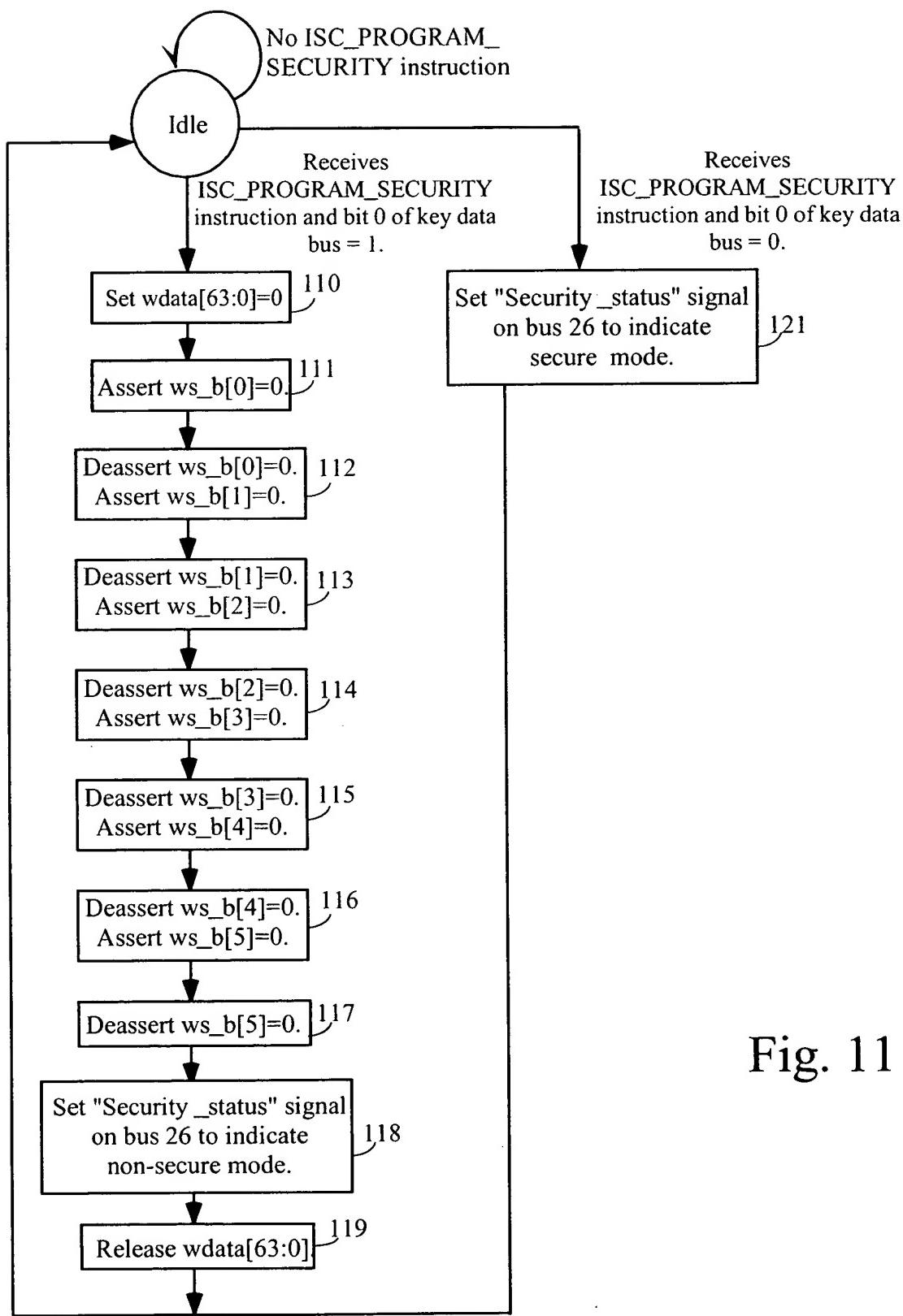


Fig. 11

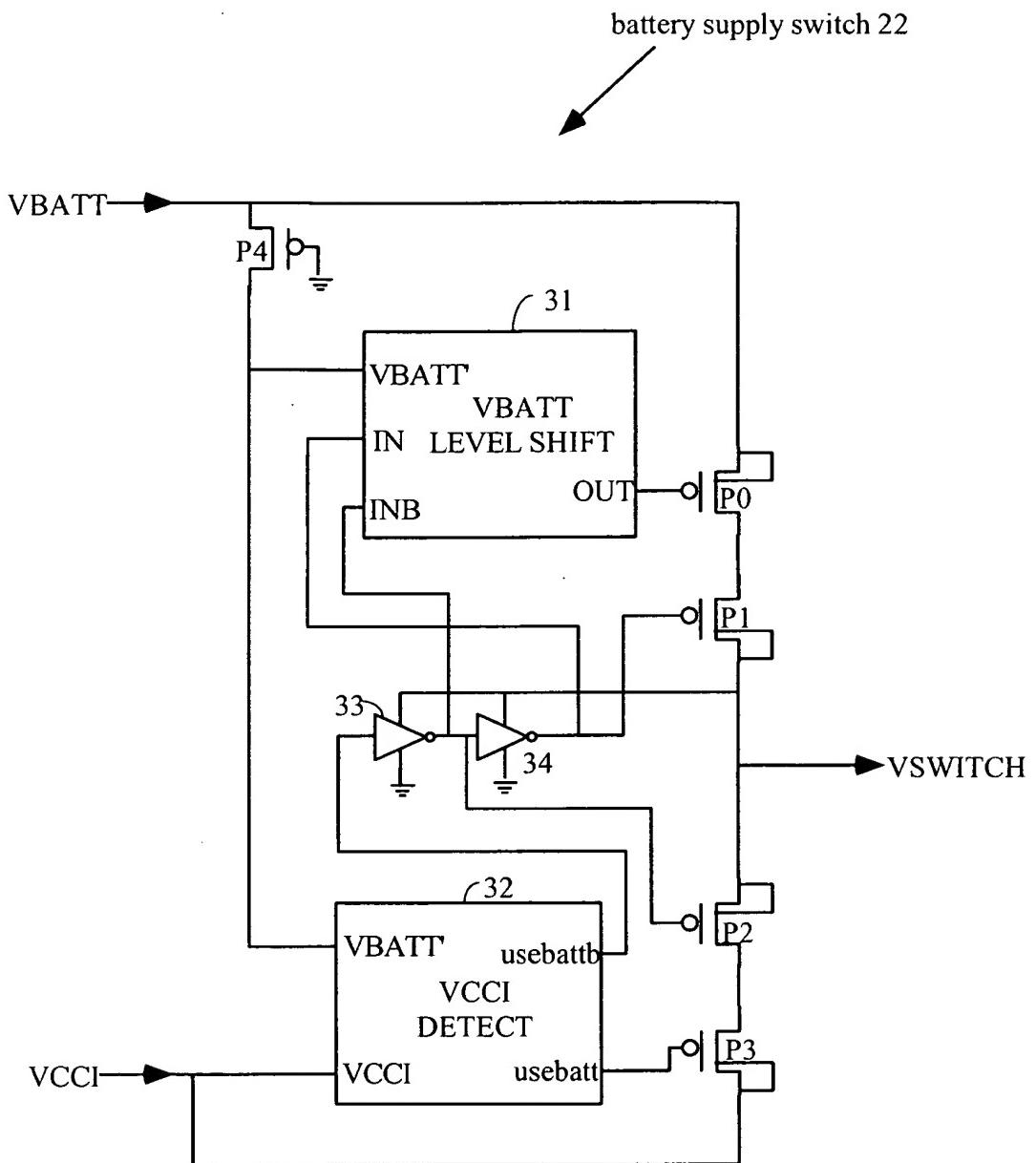


Fig. 12

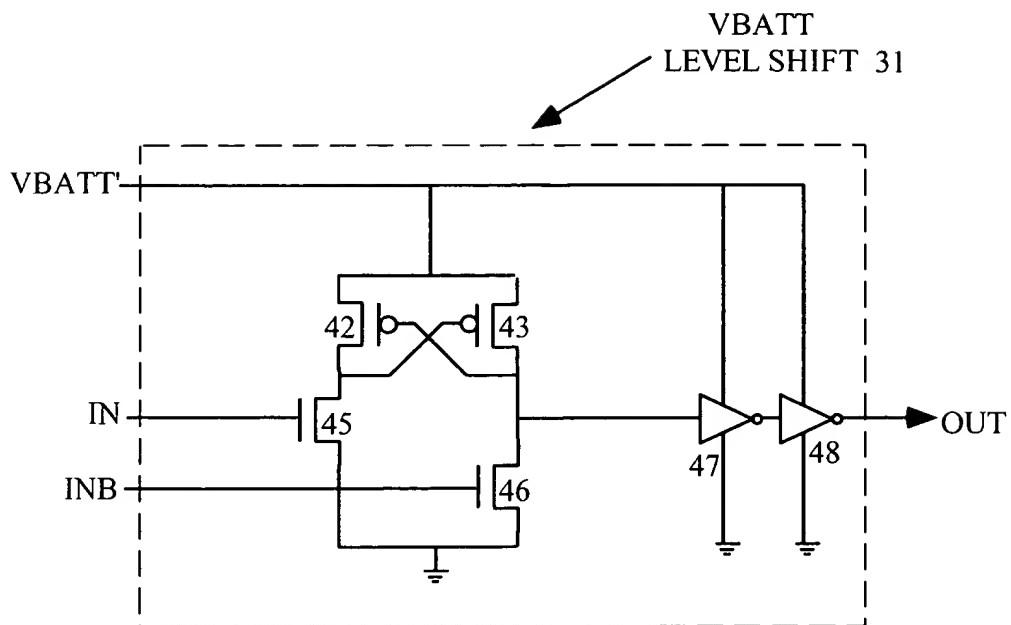


Fig. 13

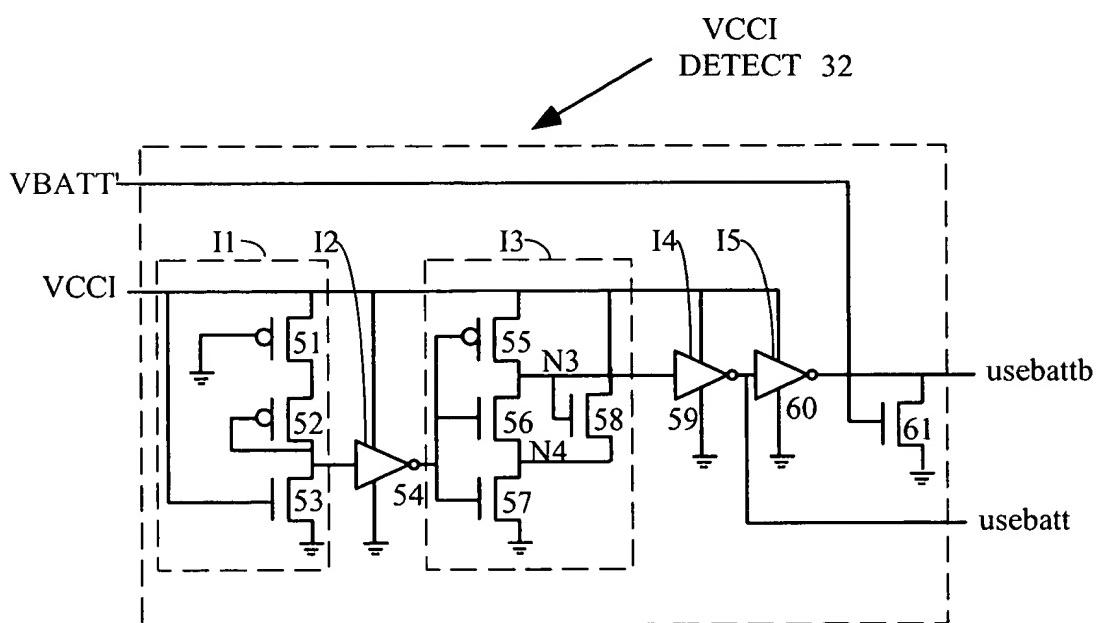


Fig. 14

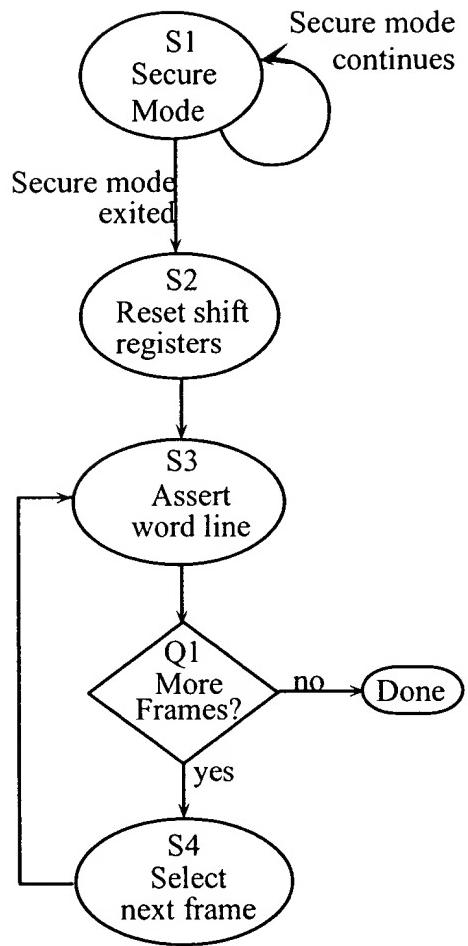


Fig. 15

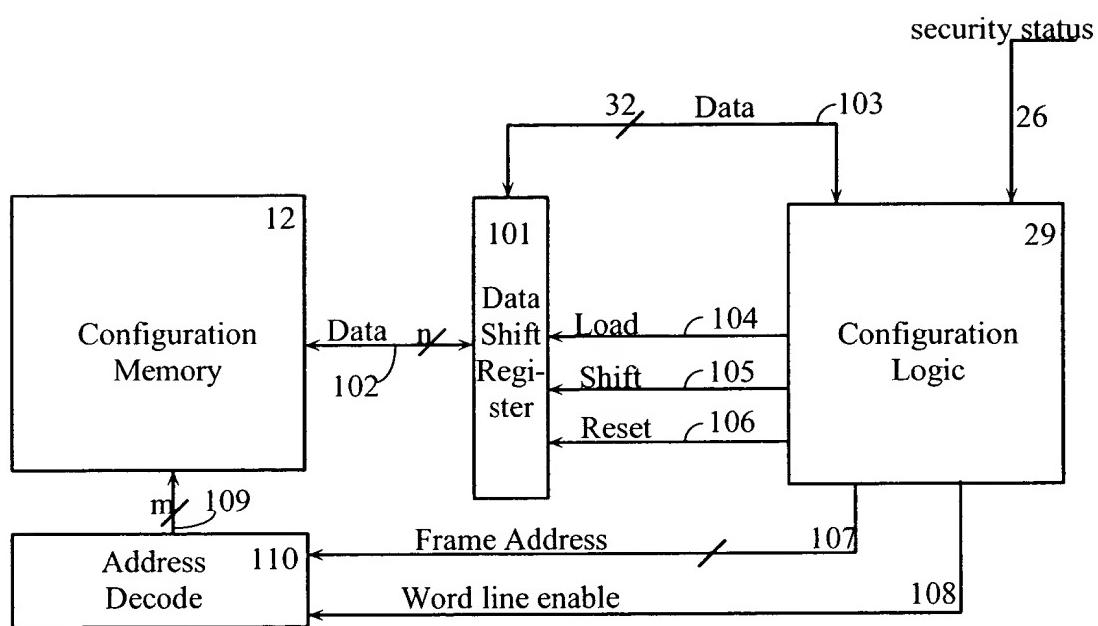


Fig. 16